NANDHA

College of Technology

(Autonomous)

Approved by AICTE, NewDelhi | Affiliated to Anna University, Chennai

Erode, Tamilnadu

THIRU.V.SHANMUGAN
(Chairman)

CHAIRMAN'S MESSAGE:

I am immensely pleased to share my thoughts on this academic year 2025-26, which has been a period of continuous learning, growth, and achievement for the Department of Electronics and Communication Engineering (ECE).

The department has consistently focused on strengthening students' technical knowledge, practical skills, and innovative thinking. Emphasis has been given to core domains such as communication systems, signal processing, embedded systems, VLSI, and emerging technologies, ensuring that students are well-prepared to meet industry expectations.

Throughout the year, various academic and co-curricular initiatives have been undertaken to enhance analytical abilities, problem-solving skills, and hands-on experience. Students are encouraged to actively participate in learning beyond the classroom, enabling them to develop confidence, creativity, and a strong technical foundation.

Our institution continues to adopt modern teaching methodologies that promote quality education, independent thinking, and holistic development. The goal is to nurture individuals who are not only technically competent but also responsible professionals capable of contributing meaningfully to society.

THIRU.S.NANDHAKUMAR PRADEEEP
(Joint Managing trustee)

JOINT MANAGING TRUSTEE'S MESSAGE:

I am immensely pleased to share my thoughts on this academic year 2025-26, which has been a period of consistent progress, academic excellence, and holistic development for the Department of Electronics and Communication Engineering (ECE).

The department has been actively fostering an environment that promotes technical proficiency, innovative thinking, and practical learning. With a strong focus on core and emerging areas such as communication systems, signal processing, embedded systems, and VLSI, students are being equipped with the knowledge and skills required to meet the evolving demands of the industry.

Our institution remains committed to delivering quality education through modern teaching methodologies, encouraging independent thinking, and nurturing a spirit of inquiry among students. This approach helps in developing well-rounded individuals with strong ethical values, leadership qualities, and a sense of social responsibility.

The academic year has witnessed significant efforts in enhancing students' analytical abilities, problem-solving skills, and hands-on experience, ensuring they are well-prepared for professional challenges. We continue to strive towards achieving excellence in academics and securing outstanding placement opportunities for our students.

THIRU.S.THIRUMOORTHI
(Joint Managing trustee)

JOINT MANAGING TRUSTEE'S MESSAGE:

I am pleased to share my thoughts on this academic year 2025-26, which has been marked by steady progress, academic excellence, and overall development in the Department of Electronics and Communication Engineering (ECE).

The department continues to create a strong learning environment that encourages technical competence, innovation, and practical exposure. With emphasis on key areas such as communication systems, signal processing, embedded systems, and VLSI, students are gaining the necessary knowledge and skills to meet current industry requirements.

Our institution remains dedicated to providing quality education through effective teaching practices, fostering independent thinking, and supporting the all-round development of students. This approach enables them to grow into confident professionals with strong values and a sense of responsibility towards society.

During this academic year, consistent efforts have been made to strengthen students' analytical thinking, problem-solving abilities, and hands-on learning, preparing them to face real-world challenges. We continue to work towards academic excellence and improved placement outcomes.

I appreciate the commitment of our faculty and the active participation of our students, which together contribute to the continued progress of the department.

Dr.S.NANDAGOPAL

(Principal)

PRINCIPAL'S MESSAGE:

I am pleased to present my message for this academic year 2025-26, which has been marked by consistent progress, academic excellence, and holistic development in the Department of Electronics and Communication Engineering (ECE).

The department has been actively fostering a dynamic learning environment that emphasizes technical knowledge, innovation, and practical exposure. With focused attention on core and emerging areas such as communication systems, signal processing, embedded systems, and VLSI, students are being effectively prepared to meet the challenges of the modern technological landscape.

Our institution remains committed to delivering quality education through advanced teaching methodologies, encouraging critical thinking, and promoting the overall development of students. We strive to shape individuals into competent professionals with strong ethical values and a sense of responsibility towards society.

Throughout this academic year, significant efforts have been made to enhance students' analytical skills, problem-solving abilities, and hands-on experience, ensuring their readiness for both higher education and industry careers. The department continues to work towards achieving excellence in academics and securing commendable placement opportunities.

I appreciate the dedication of our faculty members and the enthusiastic involvement of our students, which together contribute to the sustained growth and success of the department.

Dr.R.JAYANTHI

(Head of the Department)

HOD'S MESSAGE:

I am happy to share my message for this academic year 2025-26, which has been a period of meaningful learning, steady progress, and overall development for the Department of Electronics and Communication Engineering (ECE).

Our department is committed to creating a supportive and dynamic environment that promotes strong technical knowledge, practical skills, and innovative thinking. With a focus on core subjects such as communication systems, signal processing, embedded systems, and VLSI, we ensure that our students gain both theoretical understanding and real-world application skills.

We continuously encourage students to enhance their analytical abilities, problem-solving skills, and hands-on experience, enabling them to face academic and industry challenges with confidence. Along with academics, importance is also given to co-curricular and skill-based learning to support the holistic development of our students.

Our dedicated faculty members play a vital role in guiding and mentoring students, helping them grow into competent engineers with strong values and a sense of responsibility. We are consistently working towards achieving academic excellence and improving placement opportunities for our students.

I appreciate the sincere efforts of our faculty and the enthusiastic participation of our students, which contribute to the success and growth of the department.

BELOVED FACULTIES

DR. G. SRIDHAR,PROF/ECE

Dr.S.PARTHIBAN,ASP/ECE

Dr.M.BASKARAN,ASP/ECE

Dr.T.RAJKUMAR,AP/ECE

Mr.P.VINOTHKUMAR,AP/ECE

Mrs.S.AMSAVENI,AP/ECE

Mrs.S.TAMILSELVI,AP/ECE

Mrs.K.GAYATHRIDEVI,AP/ECE

Mr.V.MARIMUTHU,AP/ECE

Mrs.L.PAVITHRA,AP/ECE

Mr.V.S.SATHEESH KUMAR,AP/ECE

FACULTIES'S MESSAGE:

We are glad to share our thoughts on this academic year, which has been a period of continuous learning and growth for the students of the Department of Electronics and Communication Engineering (ECE).

As faculty members, our primary focus has been to guide students in developing strong technical knowledge, practical skills, and innovative thinking. Through effective teaching and continuous support, we strive to help students understand core subjects such as communication systems, signal processing, embedded systems, and VLSI in a clear and practical manner.

OUR ASSOCIATION OFFICE BEARERS

ASSOCIATION COORDINATOR
Mrs.K.Gayathri Devi,AP/ECE

ASSOCIATION SECRETARY
Mr.D.Vishal ,IV Year

JOINT SECRETARY
Mr.P.K.Iniyan,III Year

TREASURER
Ms.P.Rajasri,IV Year

JOINT TREASURER
Mr.M.Kannan,III Year

EXECUTIVE MEMEBERS

Ms.A.Pavithra,IV Year.
Ms.K.Govardhini,IV Year
Mr.S.Karthikeyan,IV Year
Mr.P.Nathish,IV Year.
Ms.U.Kanishkasri,III Year
Ms.R.Sowbarneka,III Year
Mr.M.Gunaseelam,III Year
Mr.SS.Sriharish,III Year
Mr.L.Deepak,II Year.
Mr.A.Jailani,II Year.
Ms.R.Nikshitha,II Year.

INDUSTRIAL SEMINAR ON "WORKPLACE READINESS: TRANSITIONING FROM CAMPUS TO CORPORATE"

DATE: 21st JULY 2025

The Department of Electronics and Communication Engineering organized an Industrial Seminar on "Workplace Readiness: Transitioning from Campus to Corporate" on 21st July 2025 for ECE students. The session began with a welcome address by the Head of the Department, Dr. R. Jayanthi, who introduced the Chief Guest, Ms. Mohamuda Ayusha Nafila, Senior Engineer at Nagarro Software Private Limited, Delhi.

The seminar focused on preparing students for the transition from academic life to the corporate environment. The resource person shared valuable insights into emerging technologies such as Artificial Intelligence tools, Microsoft Copilot, and modern industry trends. She also provided a clear comparison between IT and core engineering sectors, helping students understand career paths and make informed decisions.

The session included guidance on essential aspects of workplace readiness, such as interview preparation, professional communication, personal attire, confidence building, and adaptability in dynamic work environments. Practical tips and real-world examples were shared to help students understand corporate expectations and develop the skills required to succeed.

Overall, the seminar served as a valuable platform for students to gain clarity on career opportunities and enhance their professional readiness. It motivated participants to build confidence, improve their skills, and take proactive steps towards achieving their career goals.

HANDS-ON TRAINING PROGRAM ON ARDIUNO AND INTERNET OF THINGS

DATE: 31st JULY 2025

A Hands-on Training Program on Arduino and Internet of Things (IoT) was successfully organized to enhance the practical knowledge and technical skills of students in the domains of embedded systems and smart technologies. The program aimed to equip participants with the ability to design and implement IoT-based applications using Arduino microcontrollers.

The training covered essential concepts starting from the fundamentals of Arduino hardware and software to advanced topics in IoT. Students were introduced to Arduino IDE programming, including digital and analog input/output operations, PWM control, and real-time coding practices. The session also focused on sensor interfacing such as temperature, light, and ultrasonic sensors, along with actuator control using motors and relays.

Further, the program explored IoT concepts and communication protocols like Wi-Fi and MQTT, enabling students to understand how devices communicate in smart environments. Participants also gained exposure to cloud integration using platforms such as Blynk and ThingSpeak, allowing them to monitor and control devices remotely. Real-time project development and testing formed a key part of the training, helping students apply their knowledge in practical scenarios.

"From circuits to smart connections,
Turning ideas into intelligent systems,
Empowering innovation through hands-on learning,
Shaping the future with IoT and Arduino."

ACADEMIC SEMINAR ON: SIGNALS AND SYSTEMS

DATE: 13th AUGUST 2025

The Department of Electronics and Communication Engineering organized an academic seminar on Signals and Systems to enhance students' understanding of core concepts in signal processing. The seminar focused on both fundamental and advanced aspects of continuous-time signals, including their classification and analysis in time and frequency domains.

Key topics discussed during the session included Fourier and Laplace transforms, which play a crucial role in analyzing signals and systems. The resource person also provided detailed insights into Linear Time-Invariant (LTI) systems, impulse response, convolution, and the importance of frequency response in system design.

The seminar was designed to bridge theoretical knowledge with practical applications. Real-world examples were incorporated to help students understand how these concepts are applied in engineering fields such as communication systems, signal processing, and control systems.

Overall, the seminar served as a valuable learning experience, strengthening students' conceptual foundation and encouraging them to explore advanced topics in signals and systems.

"Exploring the language of signals and systems,
Where theory meets real-world application,
Igniting curiosity and deep understanding,
Inspiring innovation in every aspiring engineer."

INDUSTRIAL SEMINAR ON: RECENT TRENDS IN VLSI DESIGN

DATE: 13th AUGUST 2025

The Department of Electronics and Communication Engineering organized an Industrial Seminar on Recent Trends in VLSI Design to provide students with insights into the latest advancements in semiconductor technology. The session began with an overview of the evolution of VLSI technology and its significant role in modern electronic systems.

The seminar covered key topics such as process node scaling, low-power and high-speed design techniques, and advanced transistor architectures including FinFET and Gate-All-Around FET (GAAFET). It also highlighted System-on-Chip (SoC) integration and the growing applications of VLSI in emerging fields like the Internet of Things (IoT), Artificial Intelligence, and 5G communication systems.

Further, the resource person elaborated on the complete VLSI design flow, industry-standard tools, verification methodologies, and challenges associated with deep sub-micron technologies. Real-world examples and case studies were presented to help students understand practical industry scenarios and applications.

The seminar also emphasized career opportunities in the semiconductor industry and the essential skills required to succeed in the field. Overall, the session provided a comprehensive understanding of current trends in VLSI design, inspiring students to explore advanced learning and research opportunities in this domain.

"Small chips, powerful possibilities,
Driving the world through VLSI innovation."

IETE INAUGURATION

DATE:13 thAUGUST 2025

IETE Inauguration – Photo Highlights

The inauguration of the IETE Student Forum was conducted with great enthusiasm and active participation from students and faculty members. The event marked the beginning of various technical and professional activities under the banner of the Institution of Electronics and Telecommunication Engineers (IETE).

The ceremony was graced by esteemed dignitaries, faculty members, and student representatives, who emphasized the importance of technical societies in enhancing practical knowledge and industry readiness. The inaugural session included lighting of the lamp, welcome address, and an introduction to the objectives and vision of IETE.

The photo highlights capture memorable moments from the event, showcasing student engagement, guest interactions, and the vibrant atmosphere of the inauguration. The event set a strong foundation for future technical initiatives, workshops, and knowledge-sharing sessions aimed at empowering students in the field of electronics and communication engineering.

The inauguration served as a platform to motivate students to enhance their technical skills, leadership qualities, and professional competencies. It also laid a strong foundation for future initiatives such as seminars, guest lectures, industrial visits, and hands-on training programs under the IETE banner.

Overall, the event was a memorable and successful occasion, setting the tone for continuous learning and active participation in technical and professional growth activities.

"Where ideas ignite and leaders emerge,
Shaping the future of engineering together."

INDUSTRIAL SEMINAR ON CURRENT TRENDS IN THE SEMICONDUCTOR INDUSTRY

Date: 29th August 2025

The Department of Electronics and Communication Engineering organized an Industrial Seminar on Current Trends in the Semiconductor Industry for ECE students on 29 August 2025. The session commenced with a welcome address by the Head of the Department, Dr. R. Jayanthi, who introduced the Chief Guest, Mr. R. Dinesh, Advisory Engineer at CXL & VAS Design, IBM, Bangalore.

The seminar provided valuable insights into the latest advancements in semiconductor technologies, including cutting-edge process nodes such as 3nm and advanced transistor architectures like Gate-All-Around FETs (GAAFET). It highlighted emerging innovations such as chiplet-based architectures, 3D IC packaging, and the increasing use of wide band gap semiconductors like Silicon Carbide (SiC) and Gallium Nitride (GaN), especially in applications related to electric vehicles and renewable energy systems.

The session also focused on the growing demand for AI accelerators and high-performance computing chips, which are driving rapid technological progress in the industry. Additionally, discussions were held on global supply chain diversification and emerging domains such as quantum computing and neuromorphic engineering.

The seminar further addressed key challenges faced by the semiconductor industry, including high fabrication costs, shortage of skilled professionals, and sustainability concerns. Overall, the session offered a comprehensive understanding of current industry trends, emphasizing innovation, heterogeneous integration, and the need for sustainable growth in the evolving semiconductor landscape.

"Smaller nodes, greater possibilities,
Driving the world with semiconductor advancements."

INDUSTRIAL SEMINAR ON PCB DESIGN INDUSTRY

DATE: 17th SEPTEMBER 2025

The Department of Electronics and Communication Engineering organized an Industrial Seminar for ECE students on 17 September 2025, aiming to provide industry-oriented knowledge and practical exposure. The session commenced with a welcome address by the Head of the Department, Dr. R. Jayanthi, who introduced the distinguished resource persons, Dr. B. Jaisankar (Professor), Mr. T. Shanmugaiya (Assistant Professor), and Mr. M. Ramesh (PCB In-Charge) from KPR Institute of Engineering and Technology, Coimbatore

The seminar focused on the PCB (Printed Circuit Board) design industry and provided valuable insights into modern advancements in semiconductor technologies, including 3nm process nodes and Gate-All-Around FETs. The speakers explained the crucial role of PCBs in connecting and supporting electronic components in a compact and reliable manner.

The session covered the complete PCB design flow, beginning from circuit schematic design to layout development, Design Rule Check (DRC), and generation of output files. Students were introduced to popular PCB design tools such as Altium, KiCad, Eagle, and OrCAD. Important design considerations, including trace width, spacing, via selection, and signal integrity, were also discussed in detail.

ACADEMIC SEMINAR ON TRANSMISSION LINE PROBLEMS USING SMITH CHART

Date: 07 thOctober 2025

The Department of Electronics and Communication Engineering organized an Academic Seminar on Transmission Line Problems Using Smith Chart on 07 October 2025 for ECE students. The session began with a welcome address by the Head of the Department, Dr. R. Jayanthi, who introduced the Chief Guest, Mr. T. Jayachandaran, Assistant Professor, Department of ECE, Nandha Engineering College, Erode.

The seminar focused on the significance of the Smith Chart as an essential graphical tool in transmission lines and RF systems. It provided a clear understanding of how complex transmission line problems can be analyzed and solved using graphical techniques. Key parameters such as reflection coefficient, Voltage Standing Wave Ratio (VSWR), and impedance matching were explained in a simplified and practical manner.

The session demonstrated how normalized impedances can be plotted on the Smith Chart to determine matching conditions and minimize signal reflections. It also highlighted the wide range of applications of the Smith Chart in antenna design, microwave engineering, and high-frequency communication systems.

Overall, the seminar offered valuable insights into solving transmission line problems efficiently and emphasized the importance of accurate impedance matching in enhancing the performance of RF and communication systems.

"Simplifying complex signals through graphical insight,
Mastering transmission lines with precision and clarity.

15th NATIONAL LEVEL TECHNICAL SYMPOSIUM
"TECHSPURTZ 2K25"

DATE: 24th OCTOBER 2025

The Department of Electronics and Communication Engineering of Nandha College of Technology (Autonomous), Erode, successfully organized the 15th National Level Technical Symposium, TECHSPURTZ' 2K25, on 24th October 2025 at the Placement Auditorium. The event commenced at 9:30 AM with great enthusiasm, witnessing active participation from students representing more than 20 institutions.

The event featured a wide range of technical activities including paper presentations, project expos, circuit debugging, technical quizzes, and interactive learning sessions. Participants showcased their innovative ideas, technical skills, and problem-solving abilities through various competitions and demonstrations.

The symposium served as a platform to bridge the gap between academic learning and industry expectations. The expert sessions inspired students to focus on research-oriented projects and encouraged them to develop practical engineering solutions for real-world challenges.

The event was efficiently coordinated by Mr. P. Vinoth Kumar, Assistant Professor/ECE, under the guidance of Dr. R. Jayanthi, Head of the Department, with the constant support of Dr. S. Nandagopal, Principal. Overall, TECHSPURTZ' 2K25 proved to be a vibrant and enriching experience, fostering innovation, knowledge sharing, and technical excellence among budding engineers.

"TECHSPURTZ' 2K25 ignited minds and ideas,
Showcasing talent beyond classrooms,
Inspiring innovation through collaboration,
Creating a path toward technological excellence."

NATIONAL LEVEL WORKSHOP ON DESIGN THINKING

Date: 24thOctober 2025

The Department of Electronics and Communication Engineering organized a National Level Workshop on Design Thinking on 24th October 2025 at the Placement Auditorium under the Association of ECE. The workshop aimed to introduce students and faculty members to innovative approaches in problem-solving and product development.

The session was conducted by Mr. T. Jayachandran, Assistant Professor/ECE, Nandha Engineering College, who provided valuable insights into the core principles and stages of design thinking, including empathize, define, ideate, prototype, and test. The session emphasized the importance of creativity, collaboration, and user-centric design in engineering innovation.

Participants gained a clear understanding of how design thinking methodologies can be applied to real-world engineering challenges, particularly in emerging domains such as Artificial Intelligence, Internet of Things (IoT), and Embedded Systems. The workshop included interactive discussions, brainstorming activities, and case studies that encouraged students to think beyond conventional approaches and explore innovative, research-oriented solutions.

The workshop significantly enhanced the analytical and problem-solving skills of the participants, motivating them to develop real-time applications and pursue research initiatives. It also fostered a culture of innovation and strengthened collaboration among participants from various institutions, making the event a highly enriching and impactful experience.

"From ideas to impact through design thinking,
Transforming challenges into opportunities."

INTER DEPARTMENT MEET

Date: 10th January 2026

The Department of Electronics and Communication Engineering organized an Inter Department Meet on 10 January 2026 at the ECE Laboratory under the banner of the Association of ECE. The event was conducted with the objective of fostering interdisciplinary interaction, enhancing technical knowledge, and encouraging collaborative learning among students.

The program was convened by Dr. R. Jayanthi, Head of the Department (ECE), and was efficiently coordinated by Mrs. S. Amsaveni, Assistant Professor/ECE and Association Coordinator, along with the active support of student office bearers and executive members of the ECE Association.

The meet provided a common platform for students from various departments to interact, exchange ideas, and showcase their talents through a range of technical and non-technical activities. The events were designed to enhance problem-solving abilities, teamwork, communication skills, and leadership qualities among participants.

The enthusiastic participation of students created a vibrant and competitive environment throughout the event. Overall, the Inter Department Meet was a successful and enriching experience, promoting innovation, unity, and holistic development, while fulfilling the objectives of the ECE Association and IETE Student's Forum.

INTRA DEPARTMENT MEET

DATE:12th JANUARY 2026

The Department of Electronics and Communication Engineering organized an Intra Department Meet at the ECE Laboratory under the ECE Association and IETE Student's Forum. The event was conducted with the objective of enhancing students' technical knowledge, practical skills, and professional competencies through interactive and competitive activities.

The meet featured various technical events such as Paper Presentation, Project Presentation, and Circuit Debugging. The Paper Presentation event encouraged students to explore and present recent advancements in electronics and communication, thereby improving their research aptitude, subject knowledge, and presentation skills. The Project Presentation provided a platform for students to showcase their innovative ideas and practical implementations, where they explained design methodologies, applications, and real-time relevance of their projects.

The Circuit Debugging event focused on identifying and rectifying faults in electronic circuits, helping students develop strong analytical thinking and troubleshooting abilities. All the events were conducted in a highly engaging and learning-oriented environment, fostering healthy competition among participants.

Students from different academic years actively participated with great enthusiasm, demonstrating teamwork, communication skills, and leadership qualities. Faculty members appreciated the students' involvement and technical performance. Overall, the Intra Department Meet proved to be a successful and enriching experience, promoting innovation, collaboration, and practical learning among students.

"Where ideas compete and innovation thrives,
Building skills beyond the classroom."

INDUSTRIAL SEMINAR ON "INTRODUCTION TO IOT AND SMART ELECTRONIC SYSTEMS"

Date: 02 thFebruary 2026

The Department of Electronics and Communication Engineering, Nandha College of Technology, Erode, organized an Industrial Seminar on "Introduction to IoT and Smart Electronic Systems" on 02 February 2026 at the EDC Lab under the banner of the Association of ECE. The seminar aimed to provide students with exposure to emerging IoT technologies and current industry practices.

The session was delivered by Mr. Karthick Rajamanickam, Product Developer and Test Engineer at SAN Engineering Solutions. He explained the fundamental concepts of the Internet of Things (IoT), smart electronic system design, and their applications in real-time industrial environments through practical examples.

The seminar emphasized the integration of hardware and software in developing smart systems and highlighted the importance of IoT in modern technological advancements. It provided students with a clear understanding of how theoretical concepts are applied in real-world scenarios.

Overall, the seminar served as a valuable learning platform, encouraging active participation and motivating students to explore IoT-based projects and career opportunities. It successfully enhanced students' technical knowledge and awareness of industry trends in the field of electronics and communication engineering.

"Connecting devices, creating intelligence,
Building a smarter world through IoT."

ACADEMIC SEMINAR ON "SHIELDING NETWORKS FROM INVISIBLE THREATS"

DATE:18thFEBRUARY 2026

The Department of Electronics and Communication Engineering at Nandha College of Technology is organizing an Academic Seminar titled "Shielding Networks from Invisible Threats" to enhance awareness of modern cybersecurity challenges and protection techniques in communication networks. The seminar is conducted under the Association of ECE and the IETE Student's Forum.

The session will be delivered by Mr. V. Baranidharan, Assistant Professor (SL.G), Department of ECE, Bannari Amman Institute of Technology. The seminar focuses on identifying various cyber threats and implementing effective strategies to secure communication networks.

Participants will gain valuable insights into safeguarding digital systems, understanding vulnerabilities, and applying security measures to prevent potential cyber attacks. The session is designed to encourage active participation, technical discussions, and knowledge sharing among students.

Overall, the seminar aims to strengthen cybersecurity awareness and equip students with essential knowledge to protect modern communication systems, thereby preparing them for challenges in the evolving digital landscape.

"Protecting networks from unseen threats,
Securing the future of digital communication."

MOU SIGNING CEREMONY WITH VIJAY TECHNOCRAFT, COIMBATORE

Date: 25th February 2026

The Department of Electronics and Communication Engineering at Nandha College of Technology, Erode, organized a Memorandum of Understanding (MoU) Signing Ceremony with Vijay Technocraft, Coimbatore, on 25th February 2026 at 9:30 AM in the Principal Office. The event was conducted with the objective of strengthening collaboration between academia and industry.

The MoU was signed by Mr. V. Vijayshankar, Founder and Managing Director of Vijay Technocraft, marking the beginning of a professional partnership focused on enhancing students' technical competencies and industry exposure. This collaboration aims to bridge the gap between theoretical learning and practical application.

As part of this agreement, both institutions will work together in areas such as technical training programs, workshops, internships, industrial visits, and project development. The initiative is expected to provide students with valuable hands-on experience and improve their employability skills.

The MoU also paves the way for knowledge exchange and collaborative research activities, enabling students and faculty members to gain insights into real-time industrial practices and emerging technologies in electronics and communication engineering.

Overall, the ceremony marked a significant step towards fostering industry-academia collaboration, with promising prospects for future initiatives that will benefit both students and faculty members.

"Connecting knowledge with practice,
Shaping skilled engineers for tomorrow."

EDITORIAL COMMITTEE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NEWSLETTER INCHARGE

Mrs.S.AMSAVENI,AP/ECE

NEWSLETTER EDITORS

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Mr.M.SRIHARI,II YEAR